

L Number	Hits	Search Text	DB	Time stamp
1	13	(programmable adj1 logic adj1 controller) and (current adj1 regulator)	USPAT	2004/10/26 13:54
2	22	CPU same (current adj1 regulator)	USPAT	2004/10/26 13:58
-	2	((PWM or (pulse adj1 width adj1 modulat\$3)) near3 (current adj2 regulator\$1)) same FET	USPAT	2004/10/26 09:34
-	5	((PWM or (pulse adj1 width adj1 modulat\$3)) near3 (current adj2 regulator\$1)) same (range)	USPAT	2004/02/24 11:21
-	0	(FET with (turn near3 delay)) same (current adj1 regulator\$1)	USPAT	2004/02/24 11:21
-	36	(current adj2 regulator\$1) same (range near3 output)	USPAT	2004/02/24 13:30
-	0	(current adj2 regulator\$1) same (range near3 output)	IBM_TDB	2004/02/24 13:30
-	1	(current adj2 regulator\$1) same FET	IBM_TDB	2004/10/21 14:45
-	38	current adj2 regulator\$1	IBM_TDB	2004/02/24 13:33
-	20	713/3\$.ccls. and (current adj2 regulator\$1)	USPAT	2004/02/25 10:56
-	4	((improv\$3 or increas\$3) near2 range) with (current adj1 regulator)	USPAT	2004/02/24 16:39
-	4	wide near2 range near2 (current adj1 regulator\$1)	USPAT	2004/10/20 16:25
-	944	advantage\$6 near5 FET	USPAT	2004/10/25 15:42
-	6	(advantage\$6 near5 FET) near5 delay	USPAT	2004/02/25 10:57
-	46	FET with (turn near3 delay)	USPAT	2004/06/17 11:21
-	0	(FET with (turn near3 delay)) with advantage\$5	USPAT	2004/06/17 11:21
-	0	(FET with (turn near3 delay)) with advantage\$6	USPAT	2004/06/17 11:21
-	2	(FET with (turn near3 delay)) same advantage\$6	USPAT	2004/06/17 11:21
-	2	((PWM or (pulse adj1 width adj1 modulat\$3)) near7 (current adj2 regulator\$1)) with FET	USPAT	2004/10/20 15:55
-	2	((PWM or (pulse adj1 width adj1 modulat\$3)) near7 (current adj2 regulator\$1)) with (FET or (field adj1 effect adj1 transistor))	USPAT	2004/10/20 16:43
-	7	((PWM or (pulse adj1 width adj1 modulat\$3)) near7 (current adj2 regulator\$1)) same (FET or (field adj1 effect adj1 transistor))	USPAT	2004/10/20 15:56
-	7	(operating adj1 range) with (current adj1 regulator\$1)	USPAT	2004/10/20 16:26
-	0	(FET or (field adj1 effect adj1 transistor)) with (turn adj1 on adj1 delay)	USPAT	2004/10/20 16:31
-	0	((transistor)) with (turn adj1 on adj1 delay)	USPAT	2004/10/20 16:31
-	983	(switch\$3 near2 (speed or time)) with (FET or (field adj1 effect adj1 transistor))	USPAT	2004/10/20 16:45
-	83	(switch\$3 near2 (delay)) with (FET or (field adj1 effect adj1 transistor))	USPAT	2004/10/20 16:44
-	37	(switch\$3 near2 (delay)) near3 (FET or (field adj1 effect adj1 transistor))	USPAT	2004/10/20 16:44
-	379	(switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))	USPAT	2004/10/21 13:56
-	0	((switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))) with (current adj1 regulat\$3)	USPAT	2004/10/20 16:48

-	0	((switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))) same (current adj1 regulat\$3)	USPAT	2004/10/20 16:45
-	1	((switch\$3 near2 (speed or time)) with (FET or (field adj1 effect adj1 transistor))) same (current adj1 regulat\$3)	USPAT	2004/10/20 16:45
-	64	((switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))) with (current)	USPAT	2004/10/25 13:38
-	64	((switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))) with (current)	USPAT	2004/10/20 16:48
-	15	((switch\$3 near2 (delay)) with (FET or (field adj1 effect adj1 transistor))) with (current)	USPAT	2004/10/20 16:48
-	33903	drive adj1 circuit	USPAT	2004/10/21 10:13
-	1	(drive adj1 circuit) same (output adj1 circuit) same (feedback adj1 amplifier) same (error amplifier)	USPAT	2004/10/21 10:36
-	11	(drive adj1 circuit) same (output adj1 circuit) same (feedback) same (error)	USPAT	2004/10/21 10:36
-	540	(drive adj1 circuit) with (FET)	USPAT	2004/10/21 13:56
-	5	((drive adj1 circuit) with (FET)) same (feedback) same (error)	USPAT	2004/10/21 13:59
-	233	(drive adj1 circuit) with (FET)	EPO; JPO; IBM_TDB	2004/10/21 13:56
-	62	(switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))	EPO; JPO	2004/10/21 13:56
-	4	((switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))) with (current)	EPO; JPO	2004/10/21 13:57
-	37	((drive adj1 circuit) with (FET)) same (feedback)	USPAT	2004/10/21 14:44
-	6	((drive adj1 circuit) with (FET near2 switch)) same (feedback)	USPAT	2004/10/21 14:44
-	127	(current adj2 regulator\$1) same FET	USPAT	2004/10/21 14:45
-	74	(PWM or (pulse adj1 width adj1 modulat\$3)) near3 (current adj2 regulator\$1)	USPAT	2004/10/21 14:46
-	2	((current adj2 regulator\$1) same FET) same ((PWM or (pulse adj1 width adj1 modulat\$3)) near3 (current adj2 regulator\$1))	USPAT	2004/10/21 14:50
-	1	5682287.pn.	USPAT	2004/10/21 14:50
-	142	(PWM or (pulse adj1 width adj1 modulat\$3)) near7 (current adj2 regulator\$1)	USPAT	2004/10/21 16:16
-	2345	(noise adj2 signal) near2 ratio	USPAT	2004/10/21 16:16
-	3	((noise adj2 signal) near2 ratio) same (current adj1 regulator\$1)	USPAT	2004/10/21 16:17
-	0	dominat\$5 near3 (inductive adj1 path)	USPAT	2004/10/25 13:39
-	0	dominat\$5 near3 (inductive near2 path\$2)	USPAT	2004/10/25 13:39
-	19	(advantage\$6 near5 FET) with drive	USPAT	2004/10/25 15:42
-	98	computer same (current adj1 regulator)	USPAT	2004/10/26 13:58
-	14026	voltage near2 conver\$6 near3 current	USPAT	2004/10/26 11:13
-	9	713/300 and (current adj1 regulator)	USPAT	2004/10/26 11:20
-	27	PLC and (current adj1 regulator)	USPAT	2004/10/26 13:53